

**Notice of Allowability**

Application No.

10/061,066

Examiner

Cuong V. Luu

Applicant(s)

STOICA, ADRIAN

Art Unit

2128

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 4/4/06.
2. ☒ The allowed claim(s) is/are 1-19.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.


Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |   |
|---|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)           |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                              |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance  |
|   | 9. <input type="checkbox"/> Other _____   |

  
KAMINI SHAH  
SUPERVISORY PATENT EXAMINER

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### **DETAILED ACTION**

Claims ~~1-19~~ are pending. Claims 20-27 have been canceled. Claims 1-19 have been examined.  
Claims 1-19 have been allowed.

### ***Response to Arguments***

1. Applicant's arguments, see pp. 6-7, filed 4/4/2006, with respect to claims 15, and 17-20 have been fully considered and are persuasive. The rejections of claims 15, and 17-20 have been withdrawn.

### ***Allowable Subject Matter***

**Claims 1-19 are allowed.**

The following is an examiner's statement of reasons for allowance:

2. As per claim 1, the closest art, Evolutionary Design of Electronic Devices and Circuits, Evolutionary Computation, 1999. IEEE CEC 99, July 1999, by Stoica et al, teaches obtaining from each simulation model fitness function the corresponding candidate circuit, ranking said candidate circuits in accordance said fitness functions, changing assignments of candidate circuits among said configurable circuit models, repeating the steps of producing, obtaining and ranking, whereby to perform them in successive iterations. However, it does not teach assigning each one of a plurality configurable circuit models of different levels of model resolutions to different individual ones of at least subset of said plural candidate circuits nor producing plural simulation models for each one of at least said subset of said candidate circuits by configuring each configurable model in accordance with

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the one candidate circuit, whereby to produce plurality of simulation models corresponding to different levels model resolutions as recited in the claim invention.

3. As per claim 8, the closest art, Evolutionary Design of Electronic Devices and Circuits, Evolutionary Computation, 1999. IEEE CEC 99, July 1999, by Stoica et al, teaches ranking said candidate circuits in accordance with their fitness functions but not assigning every one plurality of configurable circuit models of different levels of model resolutions to each one of at least a subset of said plural candidate circuits, producing plural simulation models for each one of at least set subset of said candidate circuits by configuring each configurable model in accordance with the one candidate circuit, whereby to produce a plurality of simulation models for each candidate circuit, obtaining from the plurality of simulation models of each candidate circuit a plurality fitness functions of the corresponding candidate circuit, and combining said plurality of fitness functions into a single fitness function of the one candidate circuit as recited in the claim invention.

4. As per claim 15, the closest art, Evolutionary Design of Electronic Devices and Circuits, Evolutionary Computation, 1999. IEEE CEC 99, July 1999, by Stoica et al, does not teach modeling plural candidate circuits at different levels of resolution over a plurality of iterations as recited in the claim invention.

**Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."**


**Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cuong V. Luu whose telephone number is 571-272-8572. The examiner can normally be reached on Monday-Friday 8:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamini Shah, can be reached on 571-272-2279. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. An inquiry of a general nature or relating to the status of this application should be directed to the TC2100 Group receptionist: 571-272-2100.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

CVL

  
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